

EXPEDITED PROCEDURE - EXAMINING GROUP 3729

#13

S/N 09/964,746PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Tom E. Pearson et al.	Examiner:	Carl J. Arbes
Serial No.:	09/964,746	Group Art Unit:	3729
Filed:	September 28, 2001	Docket No.:	884.999US1
Title:	METHOD AND STRUCTURE FOR IDENTIFYING LEAD-FREE SOLDER		
Assignee:	Intel Corporation	Customer No.:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

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Commissioner for Patents
P.O. Box 1450
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This paper is in response to the Office action mailed on July 17, 2003. Please amend the application as indicated on the following pages.

This response is also accompanied by a Petition to Revive An Unintentionally Abandoned Application Pursuant to 37 C.F.R. 1.137(b), exhibits supporting same, and the appropriate fee for submission of same.

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